

## Mask Set Errata 1

# 68HC908GP32 8-Bit Microcontroller Unit

### INTRODUCTION

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This mask set errata provides information pertaining to the power-up and power-down requirements applicable to these 68HC908GP32 MCU mask set devices:

- 3J20X
- 0K08S

### MCU DEVICE MASK SET IDENTIFICATION

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The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 0XJ66D. Slight variations to the mask set identification code may result in an altered version number, for example 1XJ66D.

### MCU DEVICE DATE CODES

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Device markings indicate the week of manufacture and the mask set used. The date is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. For instance, the date code "9115" indicates the 15th week of the year 1991.

### MCU DEVICE PART NUMBER PREFIXES

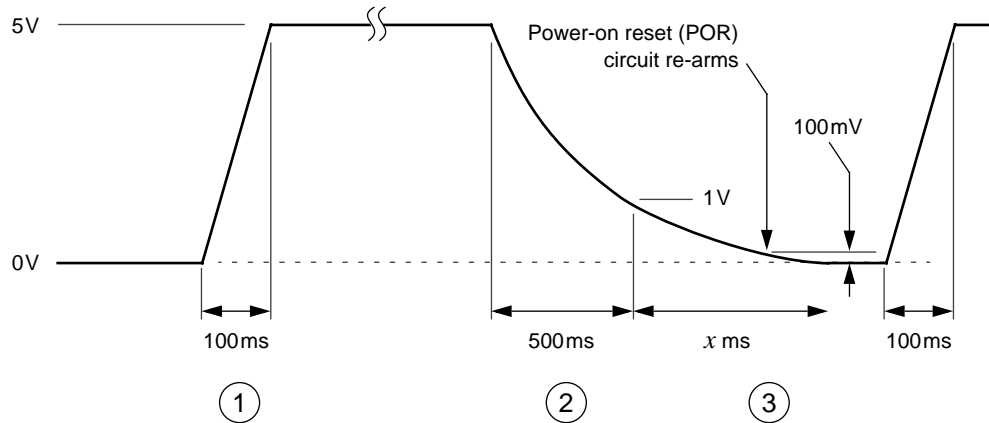
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Some MCU samples and devices are marked with an SC or XC prefix. An SC prefix denotes special/custom device. An XC prefix denotes that the device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the MC prefix.

*When contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.*


## Power-Up and Power-Down Requirements

The precautions described below must be followed for 68HC908GP32 power-up and power-down, otherwise unpredictable device behavior may occur. In severe cases, the 68HC908GP32 may experience FLASH memory erasure.



1. At power-up, supply voltage rise-time should be as short as possible; less than 143ms for 5V operation; best to aim for less than 100ms.
2. At power-down, supply voltage should fall below 1V in less than 500ms.
3. Before power-up again, supply voltage must fall below 100mV for the 68HC908GP32 power-on reset circuit to rearm.

In addition, keep the external reset pin ( $\overline{RST}$ ) pulled low at least until supply voltage has reached its operating level.

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